



- ☐ Drafts
- ☐ Pending
- ☐ Active
  - ☐ L1: (94) (1'55)
  - ☐ L3: (0) (subse
  - ☐ L2: (355) men
  - ☐ L4: (98) (1'68)
- ☐ Failed
- ☐ Saved
- ☐ Favorites
- ☐ Tagged (13)
- ☐ JDC
- ☐ Queue
- ☐ Trash

	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrie
1	☐	☐	US 20020083312 A1	20020627	28	Branch Prediction apparatus and process for restoring replaced branch	712/240		
2	☐	☐	US 6185676 B1	20010206	12	Method and apparatus for performing early branch prediction in a	712/239	712/233; 712/238	
3	☐	☐	US 5887159 A	19990323	11	Dynamically determining instruction hint fields	712/226	712/236; 712/239	
4	☐	☐	US 6401192 B1	20020604	15	Apparatus for software initiated prefetch and method therefor	712/207	711/137	
5	☐	☐	US 6567907 B1	20030520	9	Avoiding mapping conflicts in a translation look-aside buffer	711/206	711/203; 711/216	
6	☐	☐	US 6324631 B1	20011127	13	Method and system for detecting and coalescing free areas during garbage	711/170	711/159	
7	☐	☐	US 6886085 B1	20050426	33	Method and apparatus for efficient virtual memory management	711/159	711/143; 711/165	
8	☐	☐	US 20030009643 A1	20030109	31	Two-stage request protocol for accessing remote memory data in a	711/155	711/119	
9	☐	☐	US 20030126383 A1	20030703	16	Page open hint in transactions	711/154		
10	☐	☐	US 6681302 B2	20040120	15	Page open hint in transactions	711/154		
11	☐	☐	US 6128703 A	20001003	14	Method and apparatus for memory prefetch operation of volatile	711/138	711/118; 711/137	
12	☐	☐	US 20030229762 A1	20031211	13	Apparatus, method, and system for synchronizing information prefetch	711/137	711/125	
13	☐	☐	US 20030065887 A1	20030403	17	Memory access latency hiding with hint buffer	711/137		

☒ H&S
 ☒ Details
 ☒ HTXL



- ⊞ Drafts
- ⊞ Pending
- ⊞ Active
  - ⊞ L1: (1) ("20030229762").PN.
  - ⊞ L2: (1) page and 1
  - ⊞ L3: (44) prefetch\$4 near? page near? mode
  - ⊞ L4: (0) 1 and dram
  - ⊞ L5: (1) open with 3
- ⊞ Failed
- ⊞ Saved
- ⊞ Favorites
- ⊞ Tagged (1)
- ⊞ LDC
- ⊞ Queue
- ⊞ Trash

Access: US PAT: USOCR: EPO: JPO: DERWENT: IPR: TDB

04: US-PG-PUB. USPAT: USOCR: EPO: JPO: DERWENT: IPR: TDB

P: Pubs

Default operator: OR

☐ Highlight all hit terms (only)

open with 3

BRS form USAR form Image Text HTML

	U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	li
1	<input type="checkbox"/>	US 6347055 B1	20020212	16	Line buffer type semiconductor memory device capable of direct	365/189.05	365/189.01; 365/230.08		Motomura; I